

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In Re Application of:

Anand Murthy, et al.

Application No.: 09/475,452

Filed: 12/30/1999

For: A NOVEL MOS TRANSISTOR  
STRUCTURE AND METHOD OF  
FABRICATION

Examiner: Lee, Eugene

Art Unit: 2815

Confirmation No. 6341

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Respectfully submitted,

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Dated: May 13, 2008

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